rev. 11/98 kgr

	stant Co hington,	ommissioner for Patents D.C. 20231					
			DMENT TRANSMITTAL	RECEIVED			
1. Transmitted herewith is an amendment for this a				FEB 0 5 2003			
				Technology Center 260	00		
X X	(<u>21</u> Transmitte Drawing A	ed herewith is a response to an offi sheets) ed herewith are <u>6</u> sheets of Amendment 1 sheet g Documents 4 sheets	ice action for the above identified patent of Red Line drawings.	application.			
2.	Applica	ant is other than a small entity					
		Exten	sion of Term				
3.	The pr	roceedings herein are for a patent application and the provisions of 37 C.F.R. 1.136 apply.					
(a)	[]		on of time under 37 C.F.R. 1.136 le total number of months checked below	REC APR W:)			
		Extension [] one month [] two months [] three months [] four months	<u>Fee</u> \$110.00 \$410.00 \$930.00 \$1,450.00 Fee \$	RECLIVED			
lf an	additional	extension of time is required plea	se consider this a petition therefor.				
		-					
(b)	[X]		sion of term is required. However, this constitution that applicant has inadvertently of time.				

Name of Person Making the Deposit:

IN

Deposit:

.... 01/27/03

and Douglas H. Anderson

Serial No.: 09/989,808

Filed: 11/19/01

INTERRUPTS, AND DATASHEETS FOR MICROCONTROLLER PROGRAMMING

1.

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Phereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the U.S. Patent and Trademark Office, Washington, D.C., 20231, on the below date of deposit.

KATHERINE RINALDI

In re Application of: Manfred Bartz, Marat Zhaksilikov, Steve Roe, Kenneth Y. Ogami, Matthew A. Pleis

2673

Attorney Docket No.: CYPR-CD01169M

ther

NO

For: AUTOMATIC GENERATION OF APPLICATION PROGRAM INTERFACES, SOURCE CODE,

Examiner: not yest assigned

Art Unit: 2673

Signature of the Person Making the Deposit:

Attorney Docket No.: CYPR-CD01169M

Fee Calculation

The fee for claims (37 C.F.R. 1.16(b)-(d)) has been calculated as shown below:

(for other than a small entity)							
Fee Items	Claims Remaining After Amendment	Highest Number of Claims Previously Paid For	Present Extra Claims	Fee Rate	Total		
Total Claims	35	- 35 =	0	x \$18.00	\$0.00		
Independent Claims	3	- 3 =	0	x \$84.00	\$0.00		
Multiple Dependent Claim Fee (one or more, first added by this \$260.00 amendment)							
Total Fees							

PAYMENT OF FEES

5. The full fee due in connection with this communication is provided as follows:

- The Commissioner is hereby authorized to charge any additional fees associated with this [X] communication or credit any overpayment to Deposit Account No.: 23-0085. A duplicate copy of this authorization is enclosed.
- [] A check in the amount of \$
- [] Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

Please direct all correspondence concerning the above-identified application to the following address: INOLOGY CENTER 2800

WAGNER, MURABITO & HAO LLP

Two North Market Street, Third Floor San Jose, California 95113 (408) 938-9060

Respectfully submitted,

Date:

Βv

Anthony C. Murabito Reg. No. 35,295

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FEB 0 3 2003	
FILENT & TRADEN	9 9 PR-CD94169M

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

: GROUP ART UNIT:

: EXAMINER:

SERIAL NO: 09/989,808

Manfred BARTZ et al.

FILED: NOVEMBER 19, 2001

FOR: AUTOMATIC GENERATION OF APPLICATION PROGRAM INTERFACES, SOURCE CODE, INTERRUPTS, AND DATASHEETS FOR MICROCONTROLLER PROGRAMMING

RECEIVED FEB 0 5 2003 Technology Center 2600

PRELIMINARY AMENDMENT

COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

Prior to examination of the above-identified application, consideration of the following Amendments and Remarks is respectfully requested.

IN THE SPECIFICATION

Please replace the paragraph beginning at page 1, line 5 with the following new paragraph: The present invention relates to the field of programmable single-chip systems. Specifically, the present invention relates to a software program which allows a circuit designer to configure a circuit and then automatically generate a series of items to facilitate programming a microcontroller.

Please replace the paragraph beginning at page 6, line 9 with the following new paragraph: